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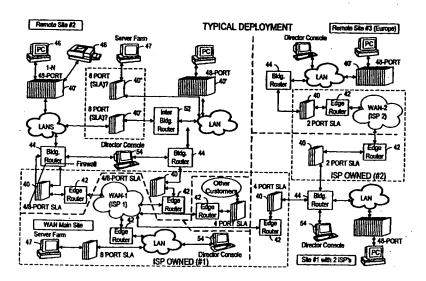
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#### (54) Title: REAL TIME MESH MEASUREMENT SYSTEM STREAM LATENCY AND JUTTER MEASUREMENTS



(57) Abstract: A method is described for measuring system latency and latency jitter on a near real-time basis regarding packets of information moving from sources (figure 1, item 46) to destinations (item 46). The method for measuring system latency and latency jitter includes time-stamping packets of information (figure 2A, item 13) as they pass through a plurality of locations between the source and destination of these packets, with the time-stamping being performed by real-time clocks set to a universal standard (figure 3). The time-stamped packets are analyzed to determine latency and latency jitter. The measurements of the system latecy and jitter can be performed based upon packet types so as to obtain accurate information concerning different types of business bandwidths associated with an observed network.

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# REAL TIME MESH MEASUREMENT SYSTEM STREAM LATENCY AND JITTER MEASUREMENTS

#### CROSS REFERENCE TO RELATED APPLICATIONS

The present application discloses subject matter which is disclosed and may be claimed in the following international applications as identified by applicant's attorney's docket nos. 402-127.2-1, 402-127.4-1, 402-127.5-1 and 402-127.8-1.

Application No. 402-127.2-1 is directed to a closed loop method for baselining business bandwidth in a network environment.

Application No. 402-127.4-1 is directed to analysis of business bandwidth for control of same. Application No. 402-127.5-1 is directed to the application of closed loop control to control of business bandwidth.

Application No. 402-127.8-1 is an extension of 402-127.2-1. 402-127.3-1, 401-127.4-1 and 402-127.5-1 with respect to exportation of information in a multiple management environment (multiple users with different SLAs).

#### TECHNICAL FIELD

The present invention is directed to an implementation/method that identifies how to create a new measuring "sense" for measuring business bandwidth performance. This new "sense" is the ability to "see" (observe & measure) in real time business bandwidth latencies and the rate of change of the latency, known as latency jitter.

#### BACKGROUND OF THE INVENTION

In a networked environment, the length of time for packets to travel from a source to a destination can vary for many reasons, including delays due to routers, communication, media, and the like. Although attempts have been made to determine latency and latency jitter through use of remote monitoring devices (RMONSs) and the like, such efforts have generally been unsatisfactory. It is desirable to make such measures in an accurate and precise manner.

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DISCLOSURE OF THE INVENTION

The present process is able to determine business bandwidth performance by determining real-

time bandwidth latency and latency jitter. Observations are continuously and simultaneously

made over the network in order to measure:

Multiple end-to-end paths.

Individual components that make up paths (those components that affect latency jitter).

This ability to continually and simultaneously measure latency (both end-to-end and for each

component) allows the measurement "sense" to identify the specific component that may be

causing latency jitter that exceeds thresholds and cause intermittent business bandwidth

Additionally, the ability to synchronize measurements taken from multiple problems.

tools/end points (and correlate/analyze in real time this information) is what is referred to as

the Real Time Mesh Measurement System.

Definitions:

Token: a single packet or packet train. It represents an application profile (such as two

packets, a pause, then three more packets).

Packet Train: a packet train simulates the traffic generated by specific applications.

Tracer: the same as "marker".

Marker: an inserted packet pair that frames (e.g., beginning and end) a stream of actual

customer traffic).

The present invention defines a new type of network service monitoring and measurement

system that can fully measure key performance characteristics of specific network service

offerings, targeted for identifying specific customer utilization and performance metrics

utilizing those specific services. This new monitoring system/method is primarily based on

the ability to insert management traffic onto a specific customer service or a specific service

provider's service channel. The two types of inserted traffic are:

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Tokens: tokens simulate different network traffic types.

Markers/Tracers: these inserted packets frame (one at the beginning and one at the end) streams of network traffic for specific services and/or specific customers.

The key components necessary to enable this new monitoring service to utilize these inserted packets and fully capture the critical performance statistics are as follows:

- Multiple hardware modules (bandwidth monitoring devices) that can be deployed throughout the network (system) that can transmit and receive these tokens and tracers.
- A distributed time service that can accurately synchronize, time stamp, and correlate the transmit and receive characteristics of the inserted traffic.
- A token building and distribution application that distributes configured/predefined tokens that will simulate customer/service traffic.
- Monitoring and performance assessment modules that can locally collect and aggregate these inserted packets and then analyze them to produce performance metrics (latency, jitter, dispersion,m and gap analysis packets within a token).
- Data collection and analysis modules that collect the raw and analyzed simulation data from multiple points and then perform further analysis to create additional performance metrics (delivery assurance, delivery reliability, effective bandwidth, and available bandwidth).

#### BRIEF DESCRIPTION OF THE FIGURES

Figure 1 is an overall diagrammatic representation of a typical deployment environment of the present invention in which various networks are interconnected for providing bandwidth necessary for implementing one or more service level agreements.

Figure 2 is a block diagram of the bandwidth monitoring device interconnect module forming part of a bandwidth monitoring device.

Figure 3 is a block diagram of the master control module of the bandwidth monitoring device.

Figure 4 is a block diagram showing how the bandwidth interconnect module and the master control module are put together to form the bandwidth monitoring device.

Figure 5 is a flow chart showing how new business bandwidth measurements,

including latency jitter and end-to-end component measurements are made according to the present invention.

Figure 6 is a flow chart illustrating how the distributed tool time clock is implemented.

Figure 7 is a flow chart showing how measured transaction latency is measured.

Figure 8 is a flow chart showing how token latency and token jitter are measured.

Figure 9 is a high level block diagram of the director console according to the present invention.

Figure 10 is a block diagram of the director console interfaces with blocks used for determination of latency and latency jitter identified with the lettering LJ.

Figure 11 is a block diagram of the director console control interface module.

Figure 12 is a block diagram of the director console control data flow with modules associated with determination of latency and latency jitter identified with the lettering LJ.

Figure 13 is a block diagram of the database analysis with modules associated with latency and latency jitter identified with the lettering LJ.

Figure 14 is a block diagram of database access associated with latency and latency jitter measurements.

Figure 15 is an overall block diagram of the director console architecture for service level agreement monitoring controls.

Figure 16 is a block diagram of the director console appliance interface with modules associated with latency and latency jitter identified with the lettering LJ.

## THE BEST MODE FOR CARRYING OUT THE INVENTION

As best seen in Figure 1, a typical deployment of the present invention comprises a plurality of bandwidth monitoring devices 40, such as edge devices which are typically collocated with edge routers 42 and building routers 44. Edge routers (switch fabric) are typically deployed at the edge of one wide area network (WAN) associated with an internet service provider (ISP) or between a WAN and a local area network (LAN) as shown in Figure 1. The edge devices (appliances) (bandwidth monitoring devices at the edge of different networks) are positioned so as to monitor bandwidth passing through an edge router typically to a building router 44 from whence the bandwidth is communicated through LANs, ultimately to one or more network enabled devices 46 such as PCs, printers and the like and/or to server farms 47. Depending upon the level of monitoring, a special type of bandwidth monitoring device such

as a network enabled device bandwidth monitoring device 40 particularly suited for connection to network enabled devices 46 can be employed. Such monitoring devices typically are positioned between a LAN and a plurality of network enabled devices 46. In addition, server farm bandwidth monitoring devices 40 can be positioned between server farms 47 and LANs or interbuilding routers 52 as shown in Figure 1.

For the methods according to the present invention to be implemented, in addition to the various types of bandwidth monitoring devices, the present invention further includes one or more director consoles 54 which pool and further analyze the bandwidth data associated with the bandwidth monitoring devices. It should be noted that Figure 1 is a typical deployment associated with the methodologies according to the present invention. Limitless other variations can be contemplated. The underlying concept however is for the bandwidth monitoring devices to be associated at locations where communications from one network environment to another is desired to be monitored, such as between a WAN of an internet service provider and an edge router interfaced to a building router associated with an office building and the like. As seen in Figure 4, each bandwidth monitoring device 40 includes a bandwidth interconnecting module 41 as shown in Figure 2 and a master control module 43 as shown in Figure 3. The master control module can communicate with one or more bandwidth interconnecting modules via backplane bus 45.

The present invention provides a process that identifies how to measure business bandwidth performance and in particular, to determine business bandwidth latencies and the rate of change of such latency known as latency jitter through observations that are continuously made in order to determine multiple end-to-end paths for packets traversing from a source to a destination, as well as individual components that make up such paths and which may therefore affect latency and latency jitter.

In order to determine such latency and jitter concerning business bandwidth, it is necessary that the present invention be implemented through a distribution of bandwidth monitoring devices 40, wherein real-time time stamping is performed. Details of the bandwidth monitoring device, including the bandwidth interconnecting module 41 and the master control module 43 are shown in Figures 2 and 3 respectively. Through use of these multiple physical tools the task of interacting with key components in the network for measuring transaction

latency from each bandwidth monitoring device to each key component is accomplished. These measurements are performed by collecting information from the bandwidth monitoring devices through synchronized time-stamped data and performing an analysis on such data at a director console 54.

Figures 5 - 8 are the overall flow charts which describe the implementation of the method of determining latency and latency jitter with respect to received bandwidth over a network topology such as that shown in Figure 1. These flow charts use the hardware shown in Figures 1, 2, 3 and 4 concerning the bandwidth monitoring device 40 and the director console 54. Details concerning the architecture of the director console 54 are presented in Figures 9 - 16 with specific modules associated with latency and latency jitter measurements identified by the letter "LJ". Furthermore, the flow of information with respect to the bandwidth monitoring device 40, including the bandwidth interconnection module 41 and the master control module 43, as well as the director console 54 are shown by flow lines containing numbers which sequentially represent the steps for performing the tasks and subtasks related to latency and latency jitter measurements according to the present invention.

Referring again to the flow chart shown in Figures 5 - 8, Figure 5 is particularly directed to the steps for performing new business bandwidth measurements, including latency, latency jitter and end-to-end component measurements. The flow chart shows the steps for performing such measurements. The distributed tool time clock module 70 is further described in Figure 6 with respect to the steps for obtaining distributed tool time clock measurements.

Figure 7 is a flow chart with respect to measurements of transaction latency while Figure 8 is a flow chart of the measurement process for token latency and jitter. With respect to details concerning autolearning and autobaselining, cross-reference is made to applicant's co-pending application entitled "Method for Automatically Baselining Business Bandwidth" (attorney docket number 402-127.2-1) filed on even date hereof.

Overall, the present invention has a distributed set of bandwidth monitoring devices which automatically identify key components to be measured based upon accurate real-time autolearning and autobaselining modules with respect to key users and applications which are

deemed to be important by the user. Such information can be configured by the user.

The present invention further provides the ability to deploy these distributed tools at key locations in the network such as those shown in Figure 1. The ability for the tools to interact with each other, coupled with the component measurements allows the overall system to generate latency "maps" which are a list of devices, type of latency measurements, the accuracy and the rate thereof are described in Figure 5.

Through this information, the system can configure and autotune the distributed tools for specific path measurements which thereby enables critical end-to-end and component views to be developed such as those set forth in Figure 8.

The use of virtual token ring technology on these specified paths allows stream-based messages to circle a virtual ring by enabling component to component stream latency and jitter measurements. See Figure 8 for additional details concerning such virtual token methodology.

Furthermore, the present invention provides the ability to use and configure different message types such as unicast, multicast, COS, POS, RSVP, which thereby allows for measuring variations in latency due to message type.

Time Clock Synchronization

The present invention, through its distributed tool time clock, is able to accurately synchronize these clocks coupled with the above virtual token, and allows for highly accurate latency and jitter measurements to be made for specific paths and specific components. Furthermore, the present invention provides the ability to continually calibrate and synchronize the distributed clocks based upon transaction response time and other observed or monitored data. See Figure 6 for additional details concerning the distributed time clock.

Thus the present invention provides that all latency measurements with respect to the network can be accurately time stamped. By so doing, the collection analysis and correlation of

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multiple transactions and (virtual token) stream latency measurements are made. The analysis output forms a complete end-to-end/component latency and jitter measurement (also known as Mesh). Furthermore, the ability to store time stamps for specific application messages as they are observed passing through the bandwidth monitoring devices, allows for the measuring "sense" to automatically validate (and calibrate/tune measurements) the latency measurement results. It should be pointed out that user messages are tagged as tracers as shown in the figures. This validation is accomplished by selecting specific message types to be traced and collecting/analyzing tracer message time stamps, and comparing them against Mesh measurements.

The Mesh measurements are the ability to use the Mesh tracer comparison to modify the Mesh token path (rate, specific information) so as to more accurately measure latency which allows a closed-loop tuning process for the Mesh measurements.

Figures 5 - 8 represent an operational description of how the measurement components and synchronization methods interact with each other to form this new business bandwidth (multiple end-to-end, component to component) latency and latency jitter measurements. Further details concerning the flow paths shown in Figures 5 - 8 are presented in Figure 2 for the flow paths shown therein, Figure 3 for the flow paths shown therein, and Figure 9 with respect to the director console. Further details concerning the distributed time clock and latency/jitter measurements are presented in Figures 9 - 16 and also in Figures 2A and 3A.

In Summary, the methodology according to the present invention provides a new business bandwidth measurement system that measures on a continuous and simultaneous basis, multiple end-to-end and component latency and latency jitter (also known as Mesh). The components of the business bandwidth Mesh sense include the ability to automatically identify specific points in the Mesh (end nodes, servers, tools, network products) that are critical points for observation such as high usage, important services, specific funnel points/bottleneck locations; the ability to automatically develop a latency map (list and operational state) for each latency measurement, thereby allowing for user input so as to tune the latency map; the ability to accurately synchronize multiple latency measurements and to accurately time-stamp these measurements, thereby enabling accurate remote collection and data analysis; wherein the new latency/jitter measurements based on both virtual token ring technology and

synchronized clocks and time stamps. Such measurements allow stream data types (such as voice/video/UDP) to be measured accurately for critical metrics such as latency, latency jitter verses "burst load".

Furthermore, the Mesh sense provide for the ability to collect latency measurements from multiple bandwidth measuring devices and to create a real-time Mesh view of latency and latency jitter throughout the network. It is also able to validate latency measurements by identifying application message types to be used as tracers in the Mesh; by accurately time stamping tracer messages passing through bandwidth measuring devices; and by collecting and analyzing tracer messages by a centralized validation module such as located in the director console. Furthermore, the Mesh sense components provide for the ability to continually observe change in latency which would require recommendation changes in the latency map, as well as the addition of specific points for latency measurements and the latency rate for more accuracy.

Having described the invention, what is claimed is:

A method of measuring system latency and latency jitter regarding packets of 1 information from sources to destinations, comprising the steps of: 2 time-stamping packets of information that pass through a plurality of 1) 3 locations between sources and destinations of packets, said time-4 stamping with a real-time clock set to a universal standard; and 5 analyzing the time-stamped packets to determine latency and latency 2) 6 jitter as said packets traverse from sources to destinations. 7

- A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 1, wherein a real-time mesh view of latency and latency jitter is generated regarding the entire network.
- 1 3. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 2, wherein only packets of information of a particular type or types are time-stamped.
- 4. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 3, wherein only packets of information of a particular type or types are time-stamped.
- 5. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 2, wherein tracer packets are generated and time-stamped as these tracer packets traverse the network.
- 1 6. A method of measuring system latency and latency jitter regarding packets of 2 information from sources to destinations according to claim 1, wherein tracer packets are 3 generated and time-stamped as these tracer packets traverse the network.
- 7. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 2, wherein the latency map can be tuned by a user based upon the latency measurements.

8. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 1, wherein specific points? in the network are identified as of higher priority and are locations where packets of information are time-stamped.

- 9. A method of measuring system latency and latency jitter regarding packets of information from sources to destinations according to claim 1, wherein said time-stamping, time clock is et to a universal time standard.
- 1 10. A system for measuring system latency regarding packets of information from sources to destinations, comprising:
  - means for time-stamping packets of information that pass through a plurality of locations between sources and destinations of packets, said time-stamping with a real-time clock set to a universal standard; and
  - 2) means for analyzing the time-stamped packets to determine latency and latency jitter as said packets traverse from sources to destinations.

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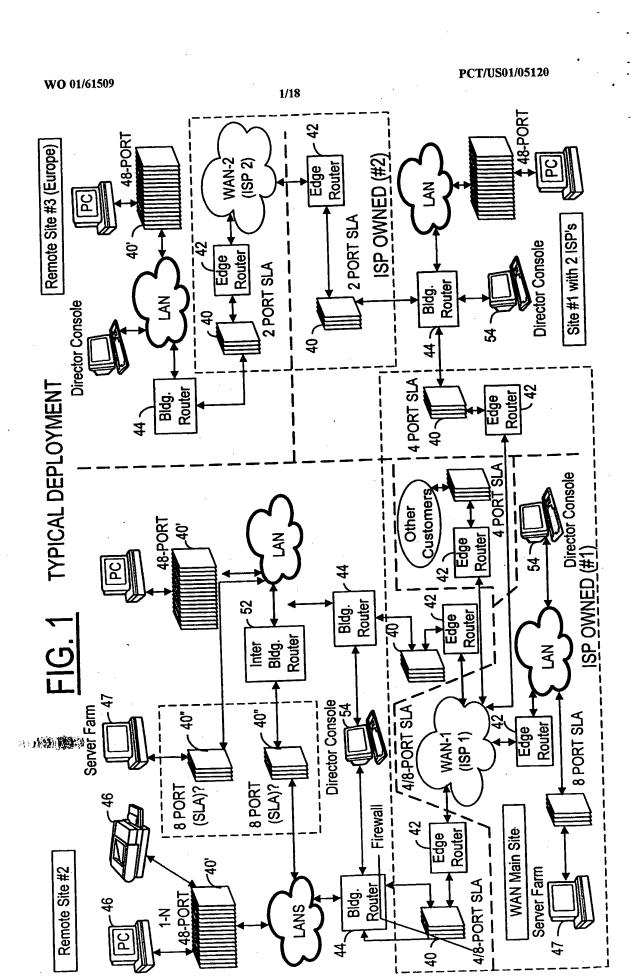
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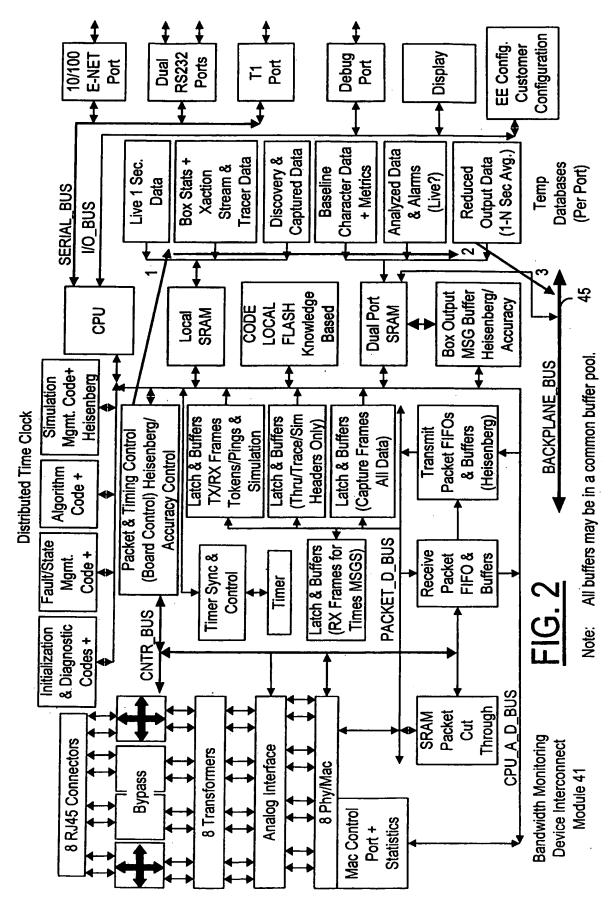
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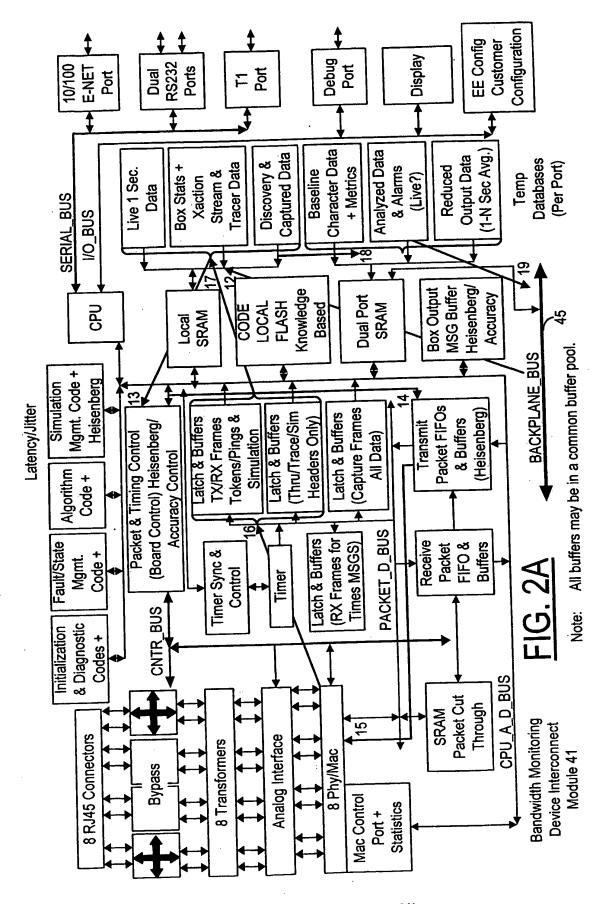
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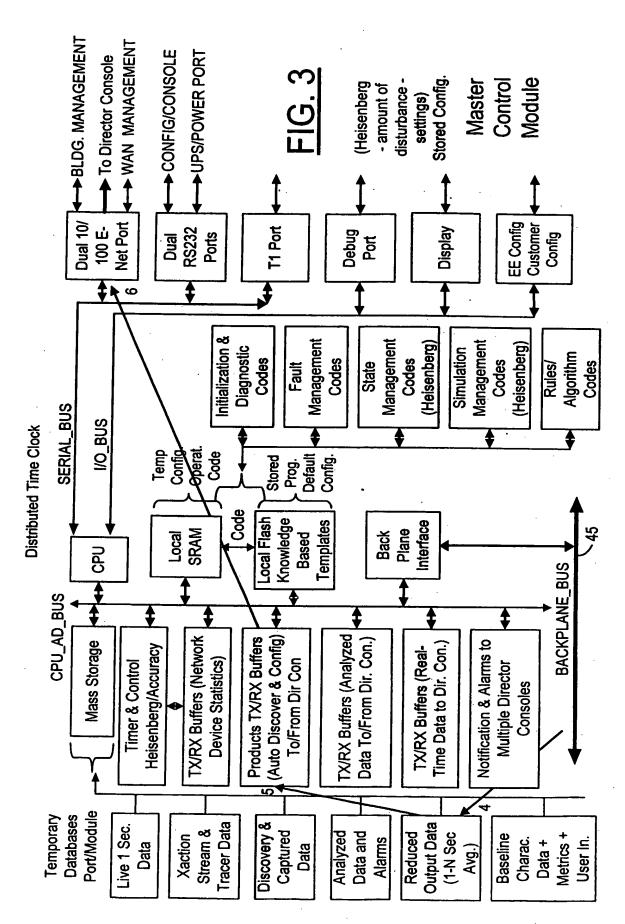
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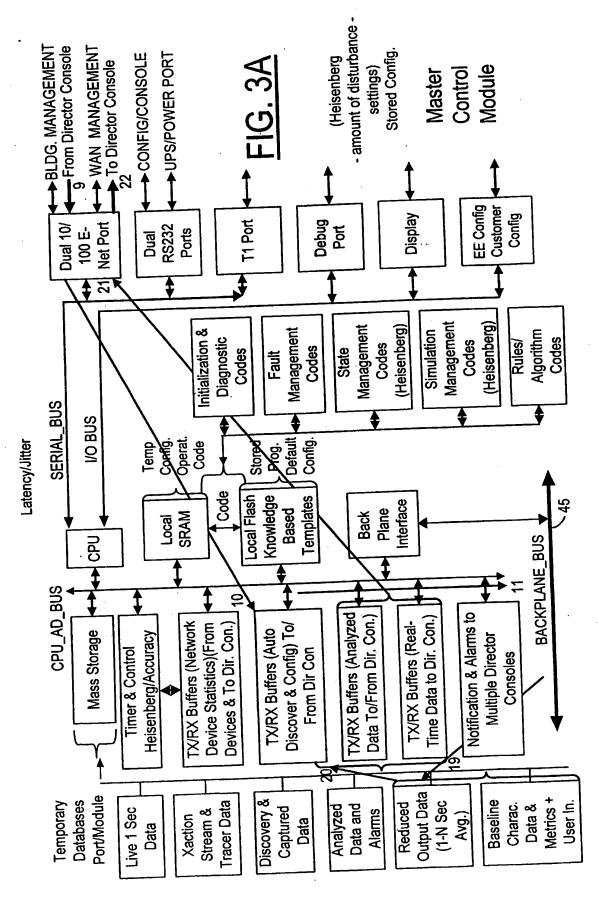




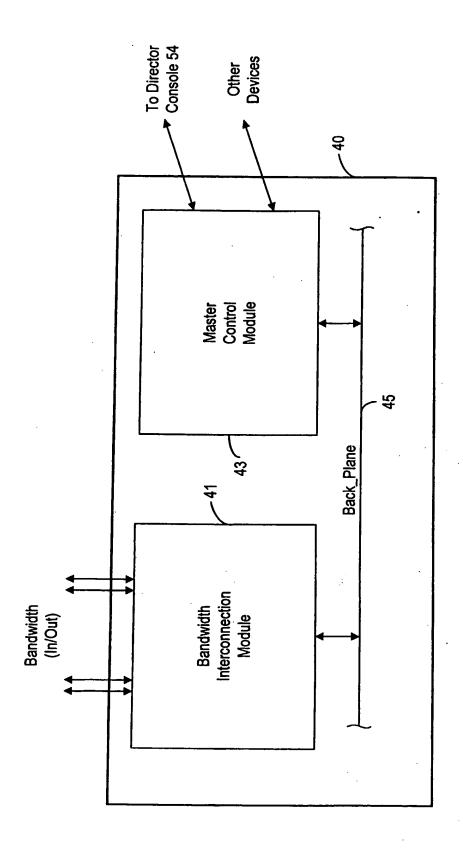
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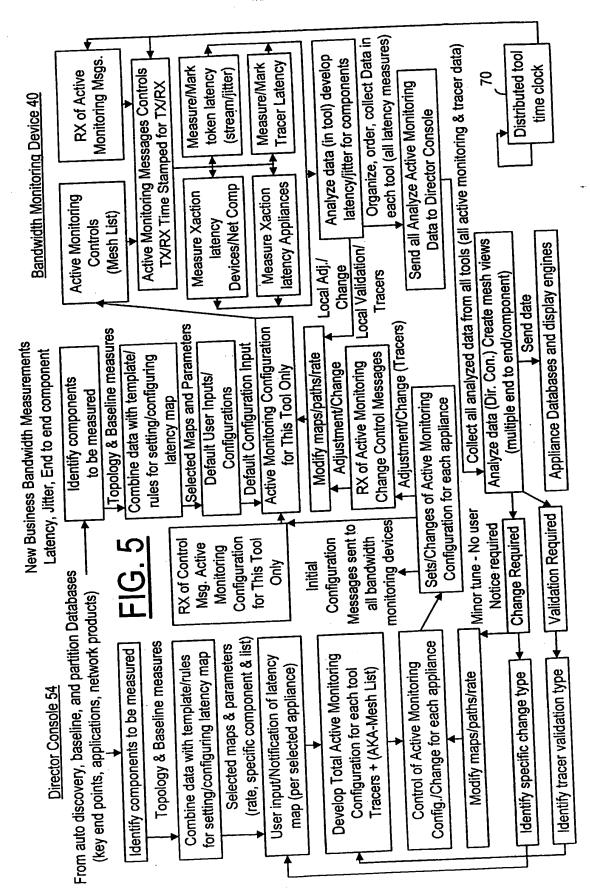




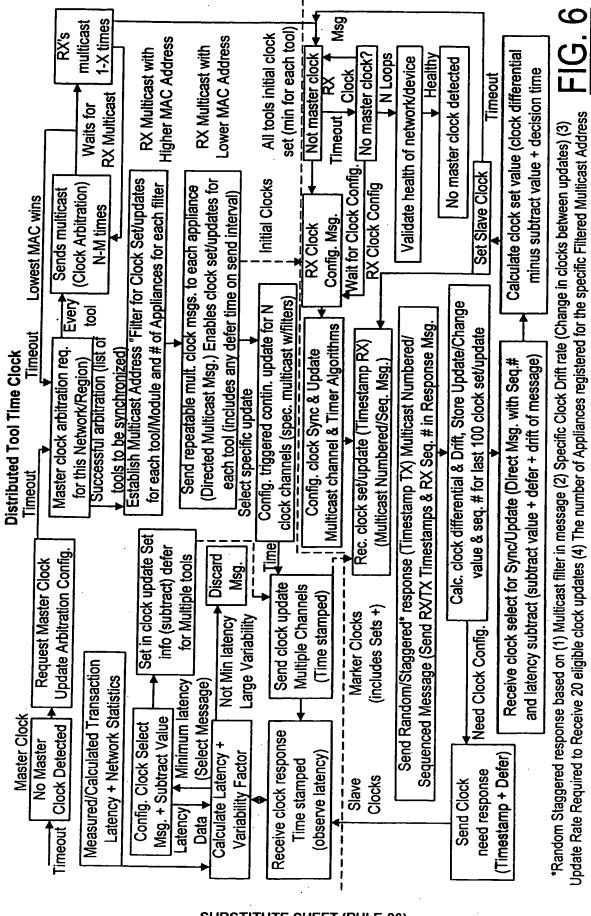
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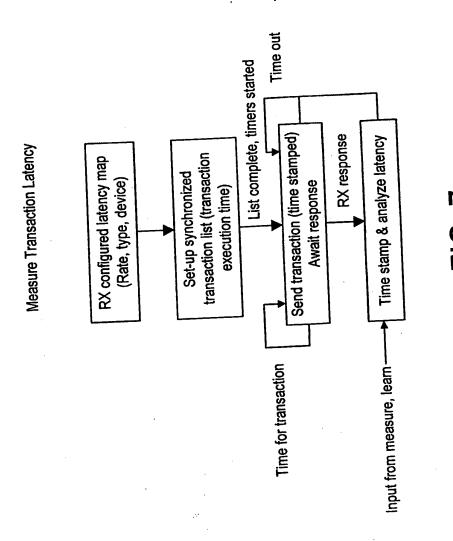


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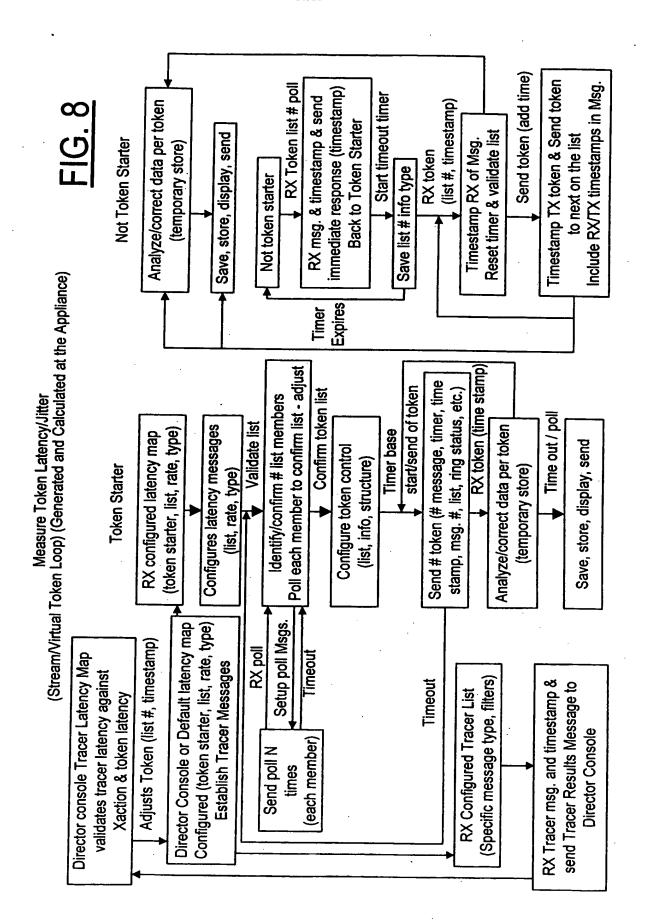


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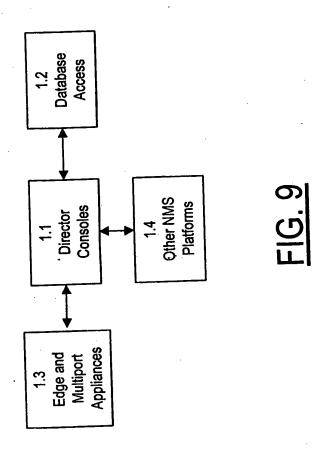


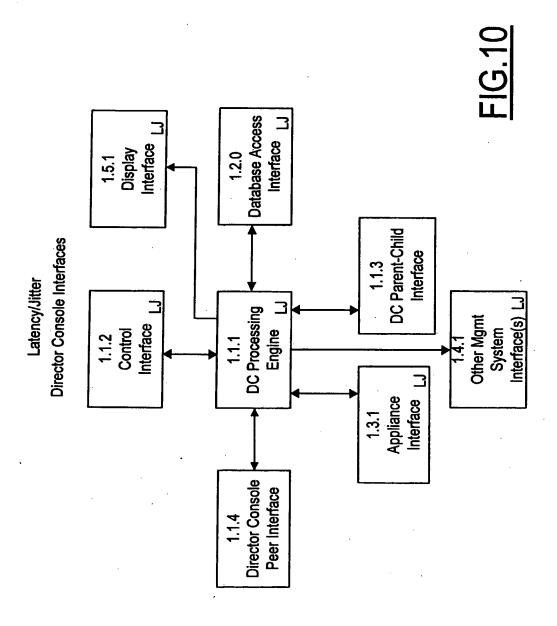


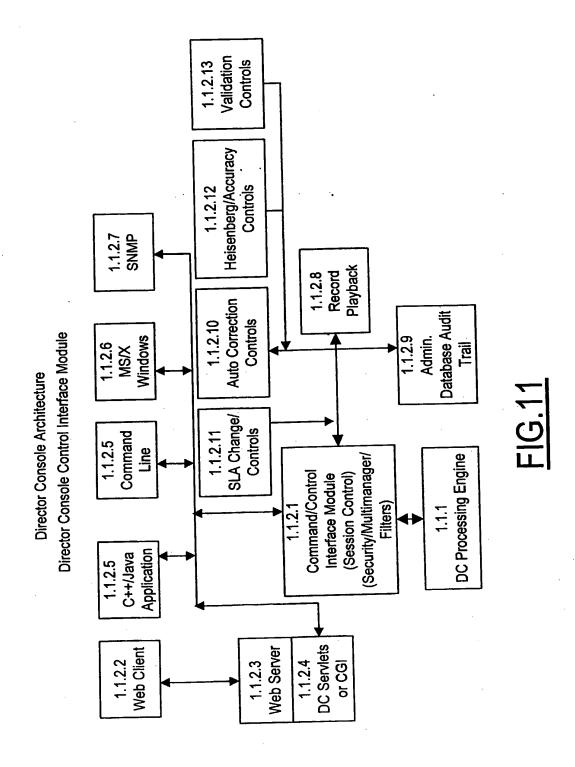
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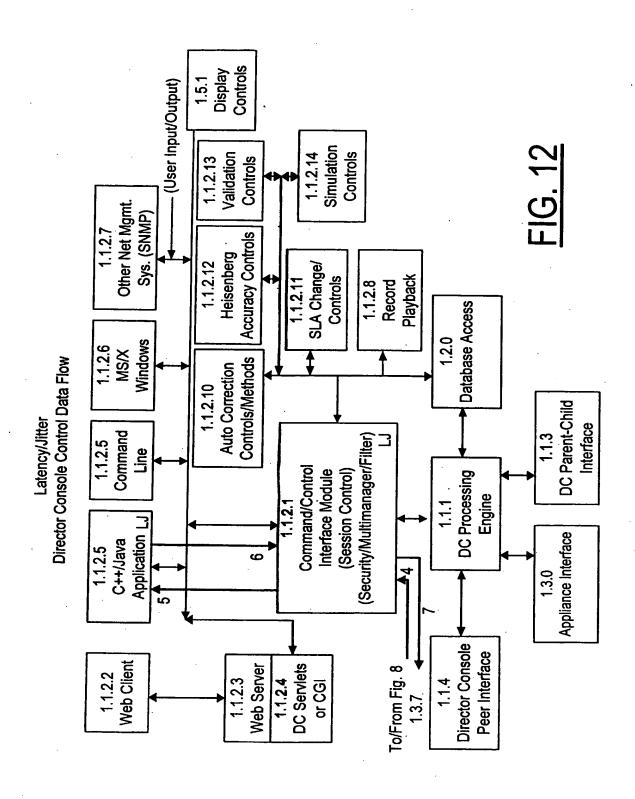
Latency/Jitter Director Console

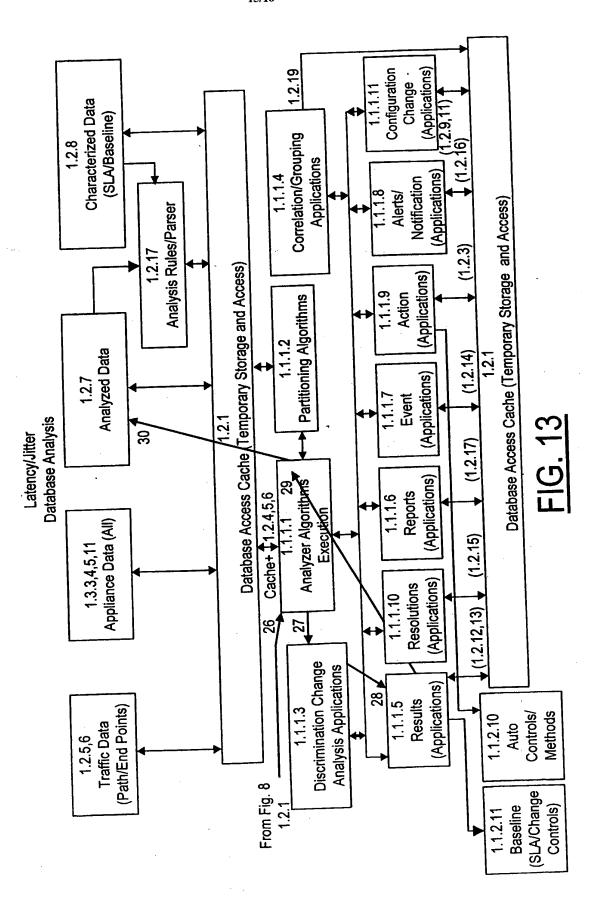




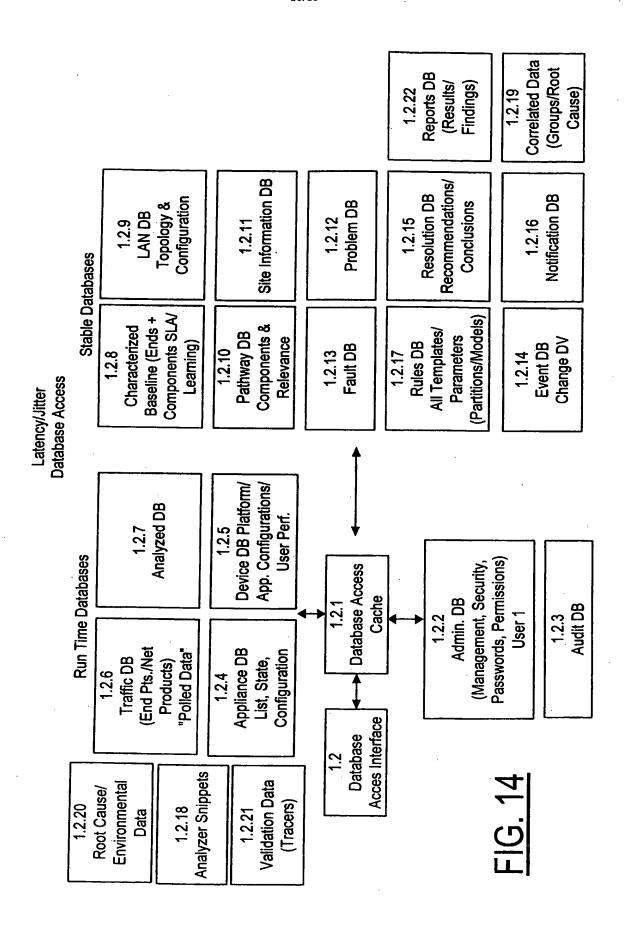


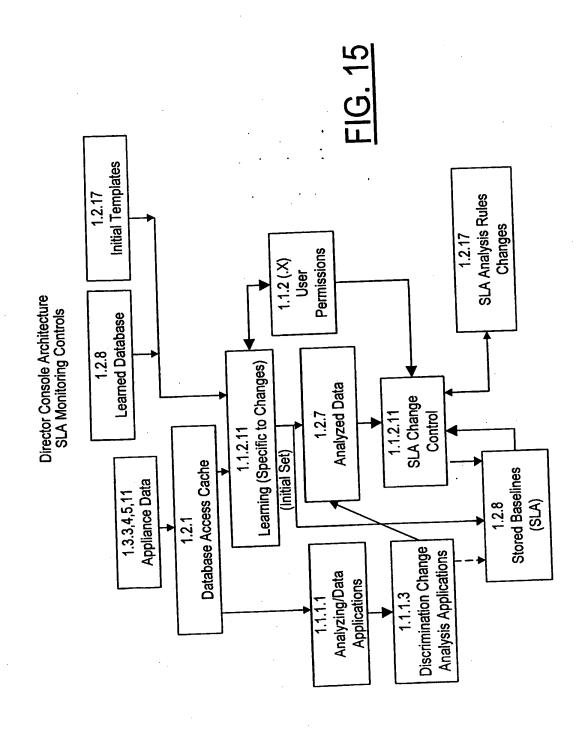
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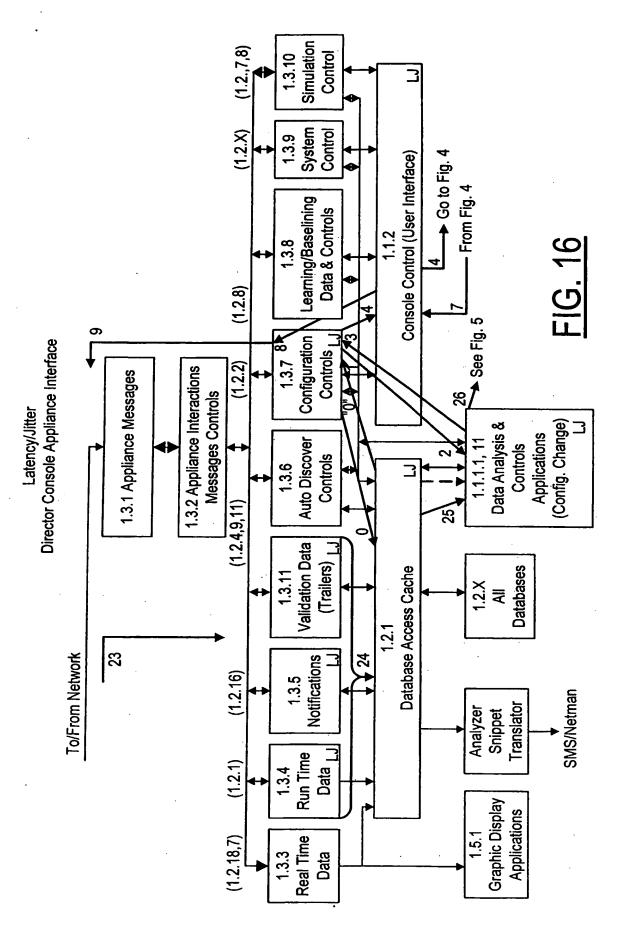




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## INTERNATIONAL SEARCH REPORT

International application No. PCT/US01/05120

A. CLASSIFICATION OF SUBJECT MATTER			
IPC(7) :G06F 13/00			
US CL: 709/233, 223, 238 According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols)			
U.S.: 709/233, 223, 238			
5.0 705/233, 223, 238			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where a	ppropriate, of the relevant passages	Relevant to claim No.
X	US 6,012,096 A (IINK ET AL) 04 JANUARY 2000		1.10
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X	US 5,944,840 A (LEVER) 31 AUGUST 1999		1-10
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X	US 5,775,996 A (OTHMER ET AL) 07 JULY 1998		1-10
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